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SFP-10G-M 10G Ethernet SFP+ Transceiver

SFP+, LC Connector, 850nm VCSEL with PIN Receiver, Multi Mode, 300M

Applications

- High-speed storage area networks
- Computer cluster cross-connect
- Custom high-speed data pipes
- 10GE Storage, 8G Fiber Channel
- Inter Rack Connection

Features

- 10Gb/s serial optical interface compliant to 802.3ae 10GBASE-SR
- Electrical interface compliant to SFF-8431 specifications for enhanced 8.5 and 10 Gigabit small form factor pluggable module "SFP+"
- 850nm VCSEL transmitter, PIN photo-detector
- 2-wire interface for management specifications compliant with SFF 8472 digital diagnostic monitoring interface for optical transceivers
- Operating case temperature: 0 to 70°C
- All-metal housing for superior EMI performance
- Low power consumption
- Advanced firmware allow customer system encryption information to be stored in transceiver
- Cost effective SFP+ solution, enables higher port densities and greater bandwidth

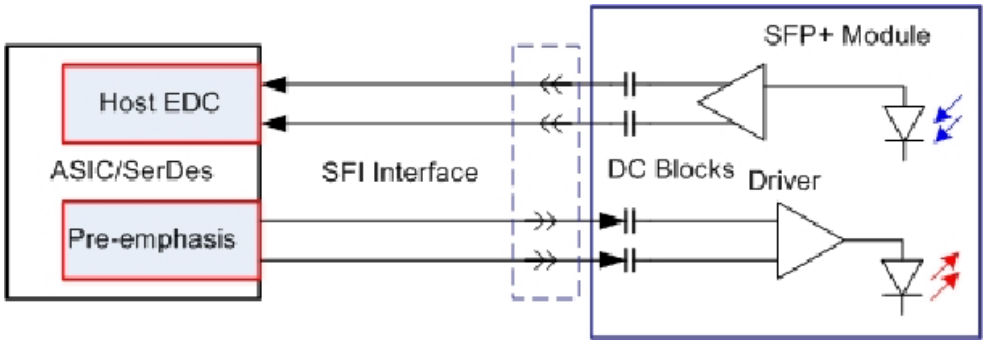

Figure1: Application in System

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1. GENERAL DESCRIPTION

This 850 nm VCSEL 10Gigabit SFP+ transceiver is designed to transmit and receive optical data over 50/125 μm or 62.5/125 μm multimode optical fiber (Table 1).

Fiber type	Minimum modal bandwidth @ 850 nm (MHz•km)	Operating range (meters)
Operating range (meters)	160	2 to 26
	200	2 to 33
50 μm MMF	400	2 to 66
	500	2 to 82
	2000	2 to 300

Table 1: SFP+ SR Operating Range for each Optical Fiber Type

The SFP+ SR module electrical interface is compliant to SFI electrical specifications. The transmitter input and receiver output impedance is 100 Ohms differential. Data lines are internally AC coupled. The module provides differential termination and reduce differential to common mode conversion for quality signal termination and low EMI. SFI typically operates over 200 mm of improved FR4 material or up to about 150mm of standard FR4 with one connector.

The transmitter converts 10Gbit/s serial PECL or CML electrical data into serial optical data compliant with the 10GBASE-SR standard. An open collector compatible Transmit Disable (Tx_Dis) is provided. A logic “1,” or no connection on this pin will disable the laser from transmitting. A logic “0” on this pin provides normal operation. The transmitter has an internal automatic power control loop (APC) to ensure constant optical power output across supply voltage and temperature variations. An open collector compatible Transmit Fault (TFault) is provided. TX_Fault is a module output contact that when high, indicates that the module transmitter has detected a fault condition related to laser operation or safety. The TX_Fault output contact is an open drain/collector and shall be pulled up to the Vcc_Host in the host with a resistor in the range 4.7-10 kΩ. TX_Disable is a module input contact. When TX_Disable is asserted high or left open, the SFP+ module transmitter output shall be turned off. This contact shall be pulled up to VccT with a 4.7 kΩ to 10 kΩ resistor

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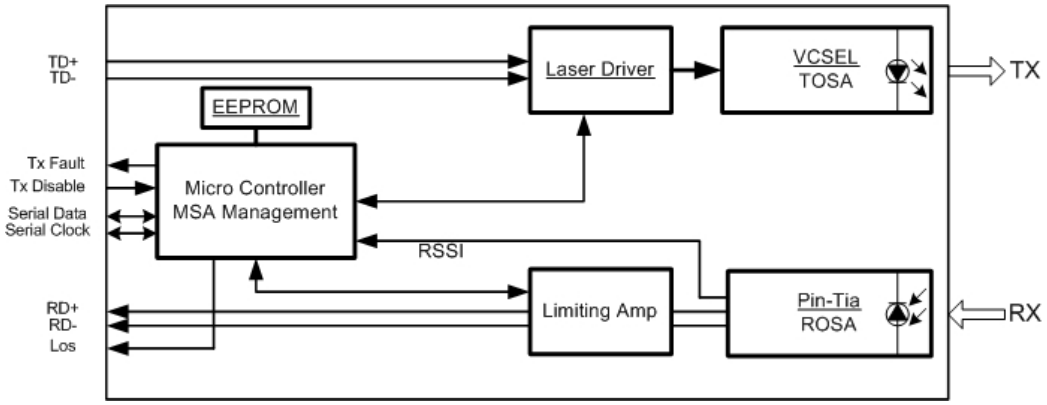
The receiver converts 10Gbit/s serial optical data into serial PECL/CML electrical data. An open collector compatible Loss of Signal is provided. Rx_LOS when high indicates an optical signal level below that specified in the relevant standard. The Rx_LOS contact is an open drain/collector output and shall be pulled up to Vcc_Host in the host with a resistor in the range 4.7-10 kΩ, or with an active termination. Power supply filtering is recommended for both the transmitter and receiver. The Rx_LOS signal is intended as a preliminary indication to the system in which the SFP+ is installed that the received signal strength is below the specified range. Such an indication typically points to non-installed cables, broken cables, or a disabled, failing or a powered off transmitter at the far end of the cable.

2. TRANSCEIVER BLOCK DIAGRAM



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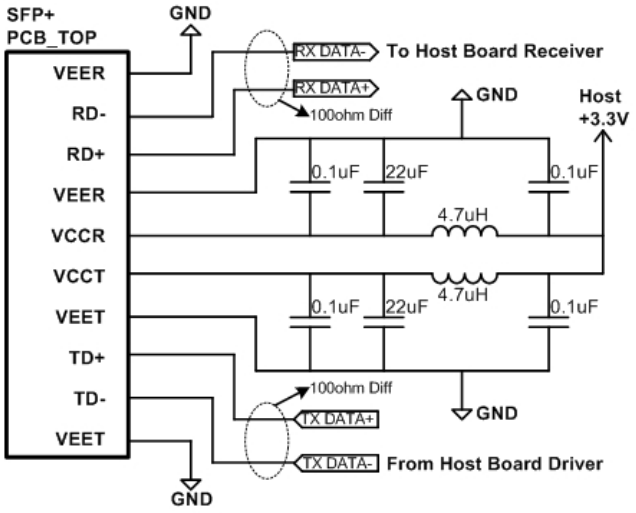
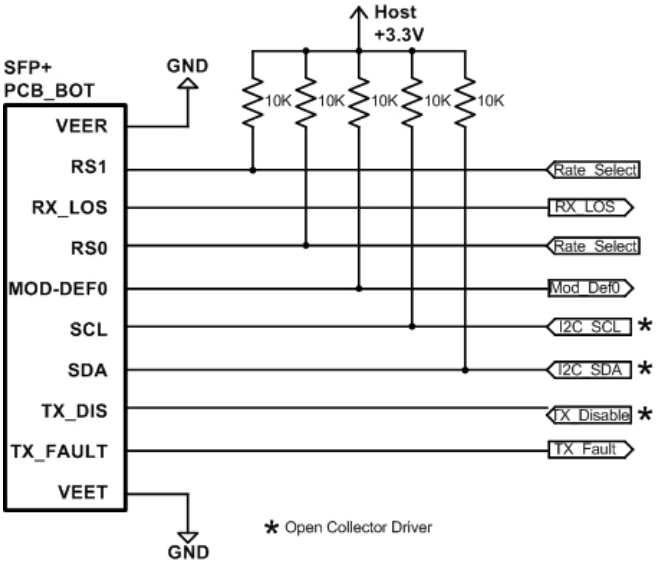
3. PROPOSED APPLICATION SCHEMATICS





4. PIN DEFINITION

The SFP+ modules are hot-pluggable. Hot pluggable refers to plugging in or unplugging a module while the host board is powered. The SFP+ host connector is a 0.8 mm pitch 20 position right angle improved connector specified by SFF-8083, or stacked connector with equivalent with equivalent electrical performance. Host PCB contact assignment is shown in Figure 2 and contact definitions are given in Table 2. SFP+ module contacts mates with the host in the order of ground, power, followed by signal as illustrated by Figure 3 and the contact sequence order listed in Table 2.

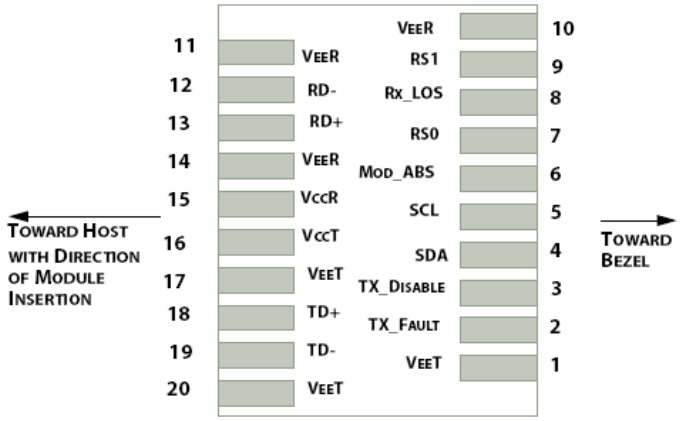


Figure 2: Module Interface to Host

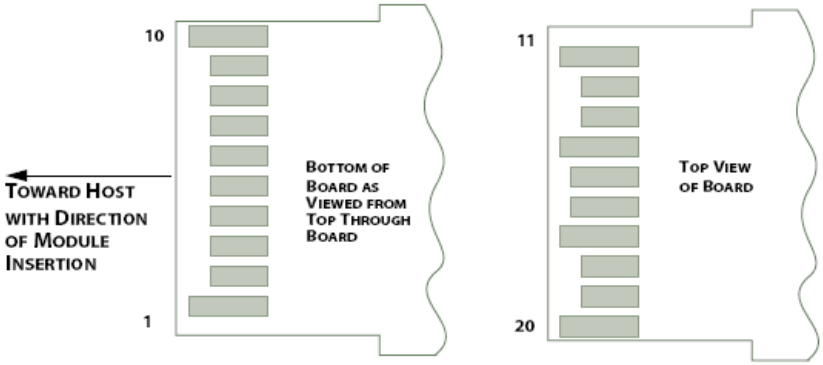


Figure 3: Module Contact Assignment

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PIN	Logic	Symbol	Name / Description	Note
1		VeeT	Module Transmitter Ground	1
2	LVTTL-O	TX_Fault	Module Transmitter Fault	
3	LVTTL-I	TX_Dis	Transmitter Disable; Turns off transmitter laser output	
4	LVTTL-I/O	SDA	2-Wire Serial Interface Data Line	2
5	LVTTL-I	SCL	2-Wire Serial Interface Clock	2
6		MOD_DEF0	Module Definition, Grounded in the module	
7	LVTTL-I	RS0	Receiver Rate Select	
8	LVTTL-O	RX_LOS	Receiver Loss of Signal Indication Active LOW	
9	LVTTL-I	RS1	Transmitter Rate Select (not used)	
10		VeeR	Module Receiver Ground	1
11		VeeR	Module Receiver Ground	1
12	CML-O	RD-	Receiver Inverted Data Output	
13	CML-O	RD+	Receiver Data Output (not used)	
14		VeeR	Module Receiver Ground	1
15		VccR	Module Receiver 3.3 V Supply	
16		VccT	Module Receiver 3.3 V Supply	
17		VeeT	Module Transmitter Ground	1
18	CML-I	TD+	Receiver Non-Inverted Data Output	
19	CML-I	TD-	Transmitter Inverted Data Input	
20		VeeT	Module Transmitter Ground	1

Note:

1. Module ground pins GND are isolated from the module case.

2. Shall be pulled up with 4.7K-10Kohms to a voltage between 3.15V and 3.45V on the host board.

Table 2: SFP+ Module PIN Definition

5. ABSOLUTE MAXIMUM RATING

These values represent the damage threshold of the module. Stress in excess of any of the individual Absolute Maximum Ratings can cause immediate catastrophic damage to the module even if all other parameters are within Recommended Operating Conditions.

Parameters	Symbol	Min.	Max.	Unit
Power Supply Voltage	V_{CC}	0	3.6	V
Storage Temperature	Tc	-40	85	°C
Operating Case Temperature	Tc	0	70	°C
Relative Humidity	RH	5	95	%
RX Input Average Power	Pmax	-	0	dBm

Table 3: Absolute Maximum Rating

6. RECOMMENDED OPERATING ENVIRONMENT

Recommended Operating Environment specifies parameters for which the electrical and optical characteristics hold unless otherwise noted.

Parameters	Symbol	Min.	Typical	Max	Unit
Power Supply Voltage	V_{CC}	3.135	3.3	3.465	V
Operating Case Temperature	T_C	0	25	70	°C

Table 4: Recommended Operating Environment

7. OPTICAL CHARACTERISTICS

The following optical characteristics are defined over the Recommended Operating Environment unless otherwise specified.

Parameter	Symbol	Min.	Typical	Max	Unit	Notes
Transmitter						
Center Wavelength	λt	840	850	860	nm	
RMS spectral width	Pm	-	-	Note 1	nm	
Average Optical Power	Pavg	-6.5	-	-1	dBm	
Optical Power OMA	Poma	-	-	Note 1	dBm	
Laser Off Power	Poff	-	-	-30	dBm	
Extinction Ratio	ER	3.5	-	-	dB	
Transmitter Dispersion Penalty	TDP	-	-	3.9	dB	
Relative Intensity Noise	Rin	-	-	-128	dB/Hz	12dB reflection
Optical Return Loss Tolerance		-	-	12	dB	
Receiver						
Center Wavelength	λr	840	850	860	nm	
Receiver Sensitivity (OMA)	Psens	-	-	-11.1	dBm	
Stressed Sensitivity (OMA)		-	-	-7.5	dBm	
Los Assert	LosA	-30	-	-	dBm	
Los Dessert	LosD	-	-	-12	dBm	
Los Hysteresis	LosH	0.5	-	-	dB	
Overload	Pin	-	-	-1	dBm	
Receiver Reflectance		-	-	-12	dB	

Table 5: Optical Characteristics

Center Wavelength (nm)	RMS Spectral width (nm)								
	Up to 0.05	0.05 to 0.1	0.1 to 0.15	0.15 to 0.2	0.2 to 0.25	0.25 to 0.3	0.3 to 0.35	0.35 to 0.4	0.4 to 0.45
840 to 842	-4.2	-4.2	-4.1	-4.1	-3.9	-3.8	-3.5	-3.2	-2.8
842 to 844	-4.2	-4.2	-4.2	-4.1	-3.9	-3.8	-3.6	-3.3	-2.9
844 to 846	-4.2	-4.2	-4.2	-4.1	-4	-3.8	-3.6	-3.3	-2.9
846 to 848	-4.3	-4.2	-4.2	-4.1	-4	-3.8	-3.6	-3.3	-2.9
848 to 850	-4.3	-4.2	-4.2	-4.1	-4	-3.8	-3.6	-3.3	-3
850 to 852	-4.3	-4.2	-4.2	-4.1	-4	-3.8	-3.6	-3.4	-3
852 to 854	-4.3	-4.2	-4.2	-4.1	-4	-3.9	-3.7	-3.4	-3.1
854 to 856	-4.3	-4.3	-4.2	-4.1	-4	-3.9	-3.7	-3.4	-3.1
856 to 858	-4.3	-4.3	-4.2	-4.1	-4	-3.9	-3.7	-3.5	-3.1
858 to 860	-4.3	-4.3	-4.2	-4.2	-4.1	-3.9	-3.7	-3.5	-3.2

* Note 1: Trade-offs are available between spectral width, center wavelength and minimum OMA, as shown in table 6.

Table 6: Minimum 10GBASE-SR OMA as a Function of Wavelength and Spectral Width

8. DIGITAL DIAGNOSTIC FUNCTIONS

The following digital diagnostic characteristics are defined over the Recommended Operating Environment unless otherwise specified. It is compliant to SFF8472 Rev10.2 with internal calibration mode. For external calibration mode please contact our sales stuff.

Parameter	Symbol	Min.	Max	Unit	Notes
Temperature monitor absolute error	DMI_Temp	-3	3	degC	Over operating temp
Laser power monitor absolute error	DMI_TX	-3	3	dB	
RX power monitor absolute error	DMI_RX	-3	3	dB	-1dBm to -12dBm range
Supply voltage monitor absolute error	DMI_VCC	-0.08	0.08	V	Full operating range
Bias current monitor	DMI_Ibias	-10%	10%	mA	

Table 7: Digital diagnostic specification table

9. ELECTRICAL CHARACTERISTICS

The following electrical characteristics are defined over the Recommended Operating Environment unless otherwise specified.

Parameter	Symbol	Min.	Typical	Max	Unit	Notes
Data Rate		-	10.3125	-	Gbps	
Power Consumption		-	600	800	mW	
Transmitter						
Single Ended Output Voltage Tolerance		-0.3	-	4	V	
C common mode voltage tolerance		15	-	-	mV	
Tx Input Diff Voltage	VI	90		350	mV	
Tx Fault	VoL	-0.3		0.4	V	At 0.7mA
Data Dependent Input Jitter	DDJ			0.1	UI	
Data Input Total Jitter	TJ			0.28	UI	
Receiver						
Single Ended Output Voltage Tolerance		-0.3	-	4	V	
Rx Output Diff Voltage	Vo	150		425	mV	
Rx Output Rise and Fall Time	Tr/Tf	30			ps	20% to 80%
Total Jitter	TJ			0.7	UI	
Deterministic Jitter	DJ			0.42	UI	

Table 8: Electrical Characteristics

10. CONTROL AND STATUS I/O TIMING CHARACTERISTICS

Timing characteristics of control and status I/O are included in Table 8, which is also defined in SFF-8431.

Parameter	Symbol	Min	Max	Unit	Condition
TX Disable Assert Time	t_off		10	µs	Time from rising edge of TX Disable to when the optical output falls below 10% of nominal
TX Disable Negate Time	t_on		1	ms	Time from falling edge of TX Disable to when the modulated optical output rises above 90% of nominal
Time to initialize, including reset of TX_Fault	t_init		300	ms	From power on or negation of TX Fault using TX Disable
TX Fault Assert Time	t_fault		100	µs	Time from fault to TX fault on.
TX Disable to reset	t_reset	10		µs	Time TX Disable must be held high to reset TX_fault
LOS Assert Time	t_loss_on		100	µs	Time from LOS state to RX LOS assert
LOS Deassert Time	t_loss_off		100	µs	Time from non-LOS state to RX LOS deassert
Rate-Select Change Time	t_ratesel		10	µs	Time from rising or falling edge of Rate Select input until receiver bandwidth is in conformance with appropriate specification.
Serial ID Clock Rate	f_serial_clock		100	kHz	

Table 9: Timing Characteristics

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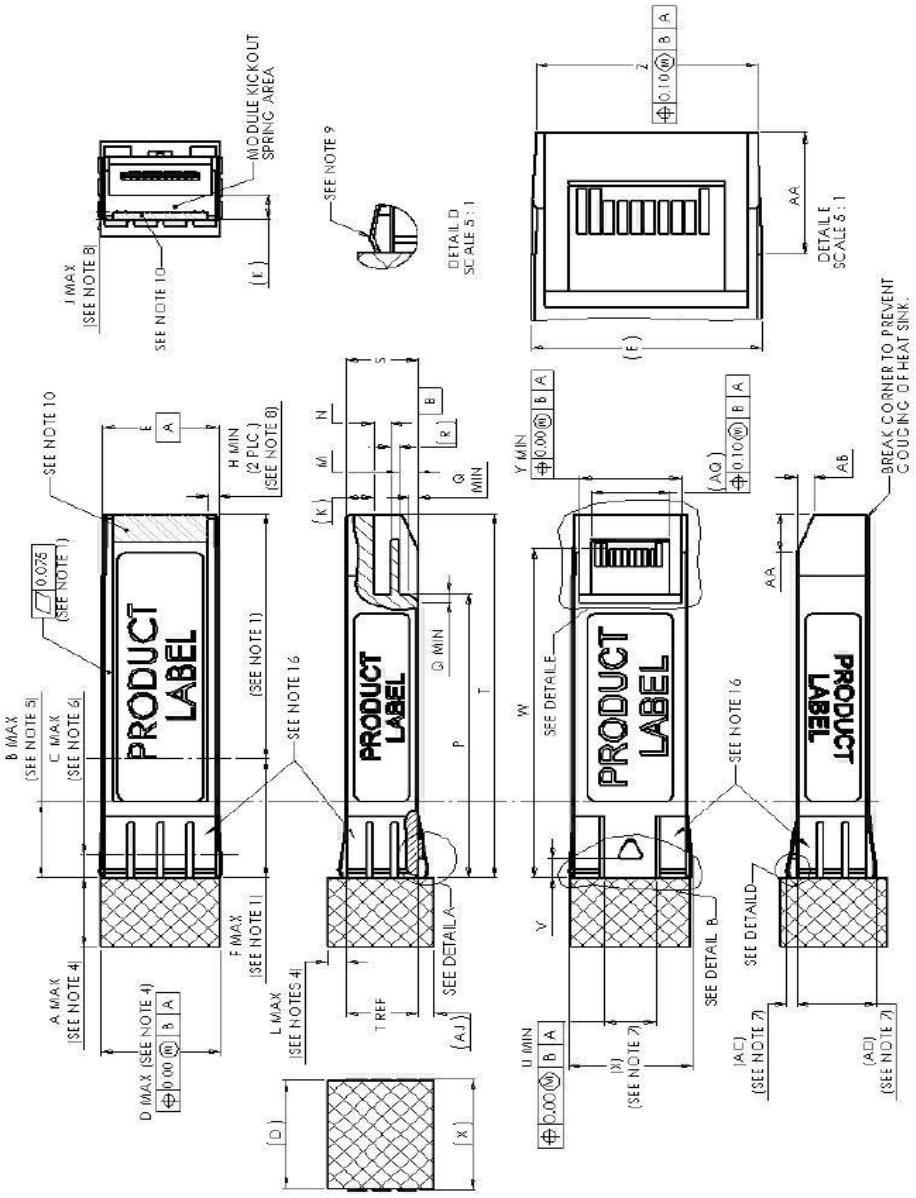
11. MECHANICAL

Complies with SFF-8432 rev. 5.0, improved Pluggable form factor specification.



Designator	Dimension (mm)	Tolerance (mm)	Comments
A	10.00	Recommended Maximum	Module length extending outside of cage, see Note 4. Other lengths are application specific.
B	10.00	Maximum	Designated EMI ground spring area, see Note 5
C	3.00	Maximum	EMI spring/Cage Contact Point, see Note 6
D	14.00	Maximum	Module width extending outside of cage, see Note 4
E	13.55	±0.25	Module width
F	15.50	Maximum	Distance to front end of optional heat sink area, see Note 1
H	1.25	Minimum	Top slot distance from edge, see note 8
J	1.00	Maximum	Top slot depth, see note 8
K	3.25	Reference	Height of module kick-out spring area
L	2.10	Maximum	Module top height extending outside of cage see Note 4
M	2.25	±0.10	Distance from bottom of Module to printed circuit board
N	2.00	±0.25	Distance from rear shoulder to printed circuit board
P	37.10	±0.30	Distance from positive stop to bottom opening of Module and beginning of bottom rear relief

Table 10: Key Mechanical Dimensions

12. ESD

This transceiver is specified as ESD threshold 2kV for all electrical input pins, tested per MIL-STD-883, Method 3015.4 /JESD22-A114-A (HBM). However, normal ESD precautions are still required during the handling of this module. This transceiver is shipped in ESD protective packaging. It should be removed from the packaging and handled only in an ESD protected environment.

13. LASER SAFTY

This is a Class 1 Laser Product according to IEC 60825-1:1993:+A1:1997+A2:2001. This product complies with 21 CFR 1040.10 and 1040.11 except for deviations pursuant to Laser Notice No. 50, dated (July 26, 2001)

Caution: use of controls or adjustments or performance of procedures other than those specified herein may result in hazardous radiation exposure.

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